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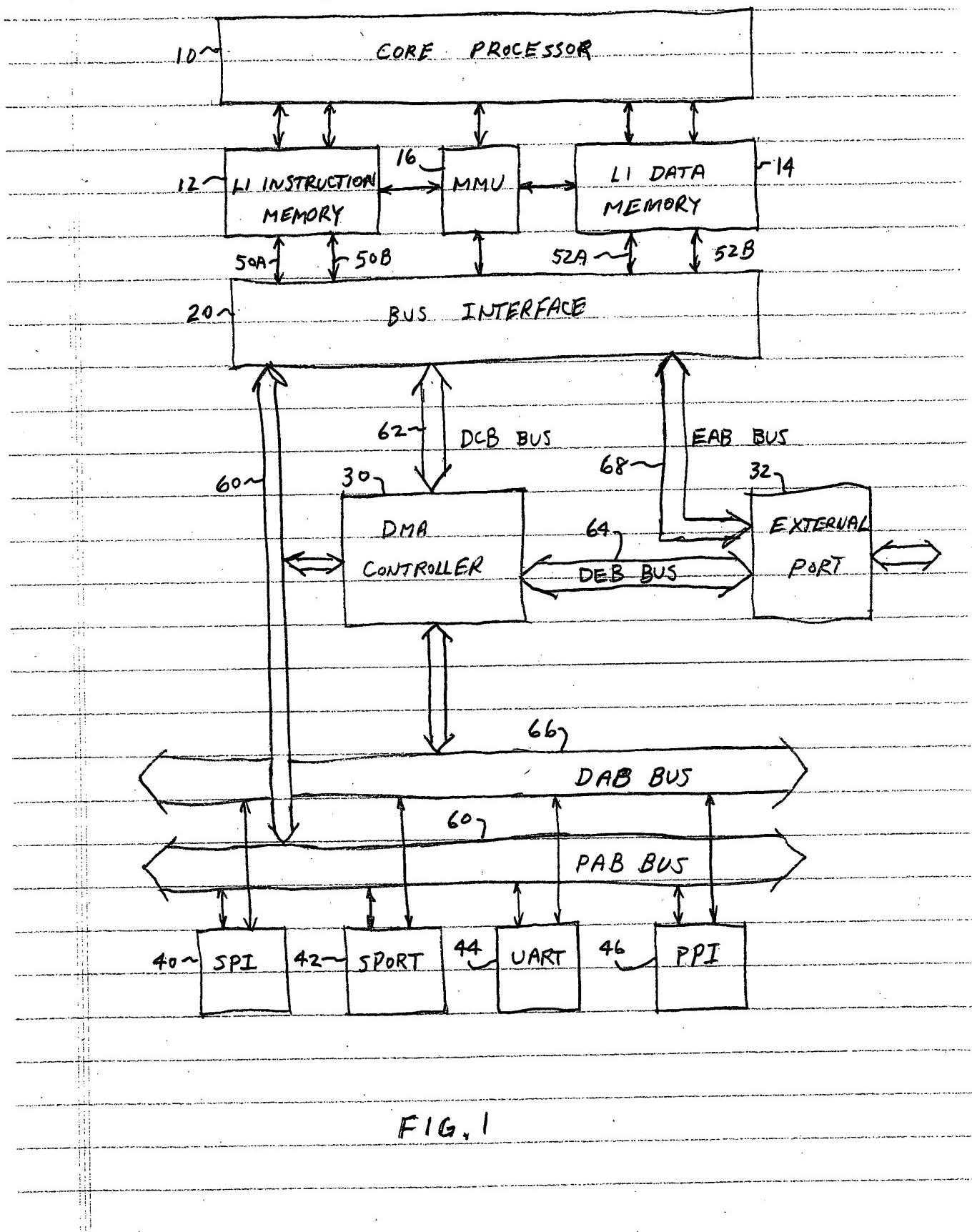
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DMA Controller Having Programmable
Channel Priority
Hayden et al.
Docket No.: A0312.70520US00



DMA Controller Having Programmable
Channel Priority
Hayden et al.
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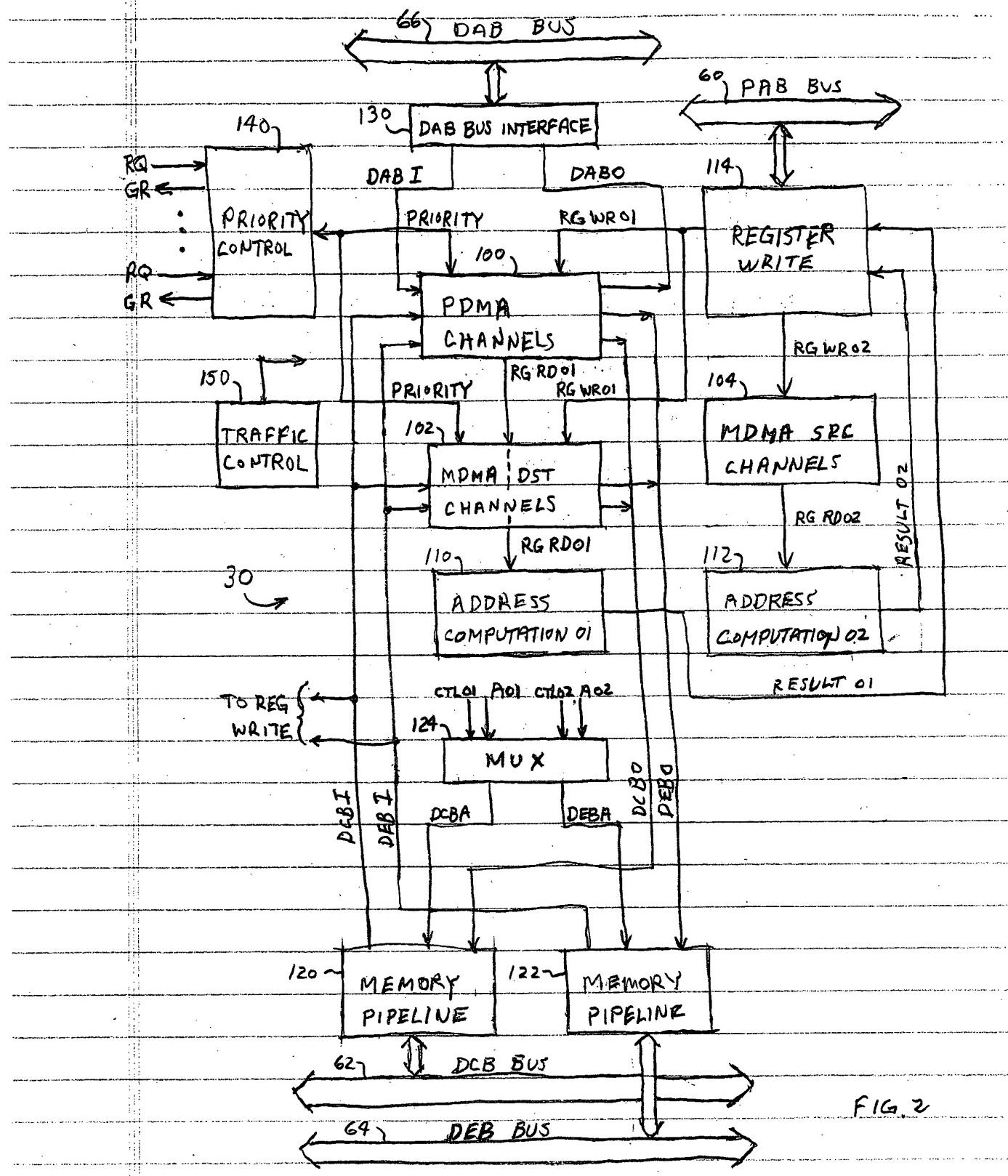


FIG. 2

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Channel Priority
Hayden et al.
Docket No.: A0312.70520US00

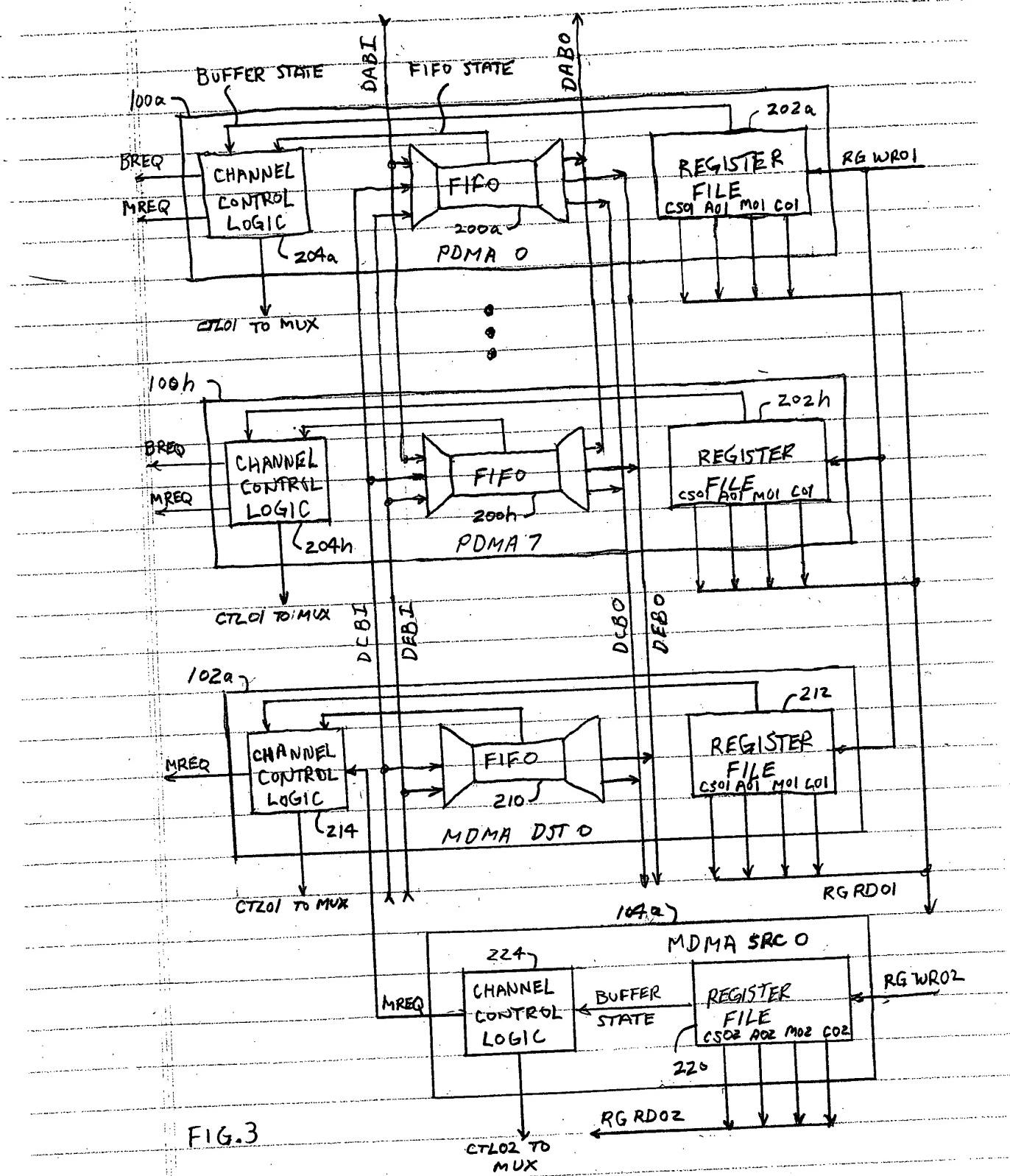


FIG.3

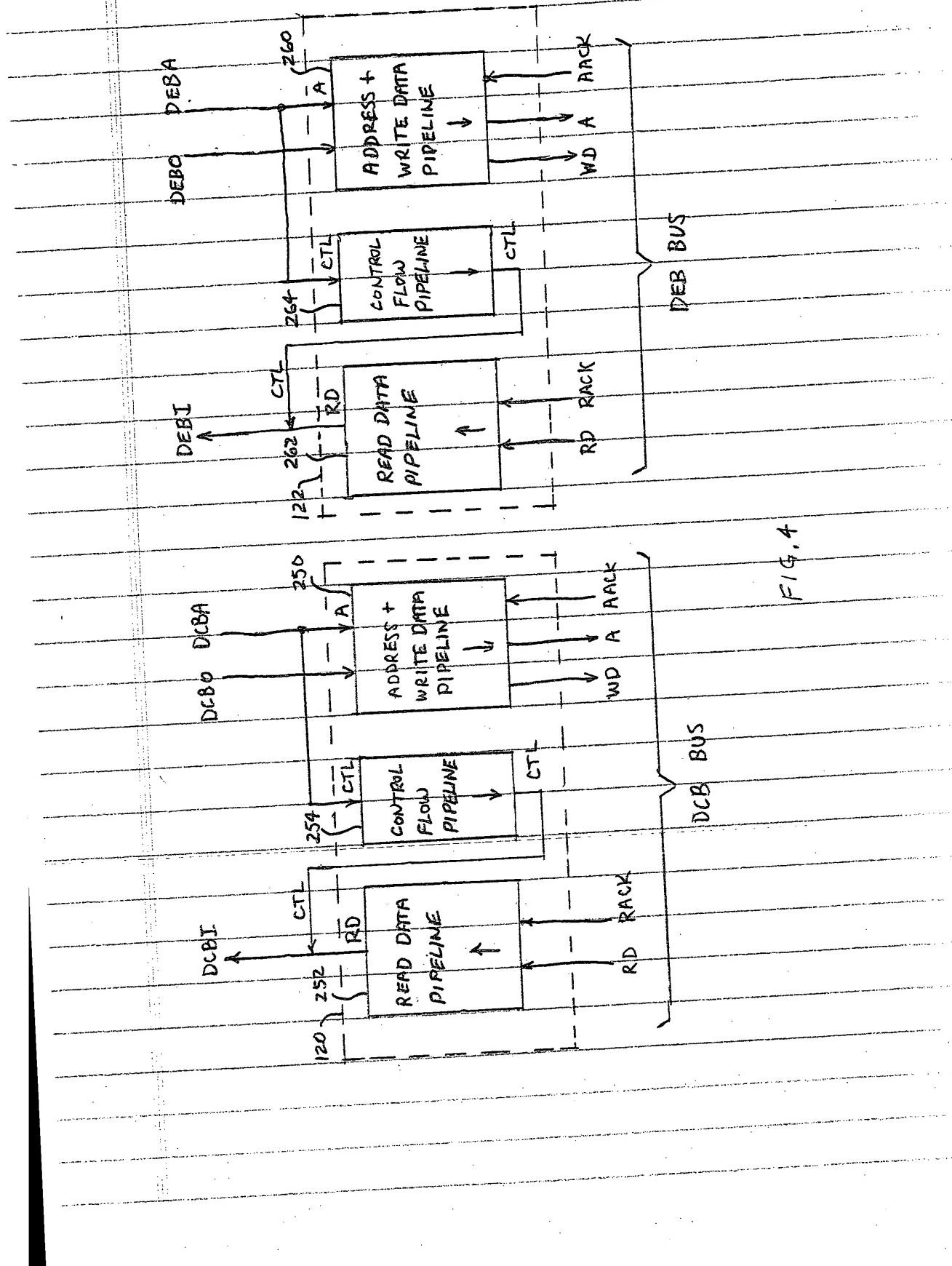


FIG. 4

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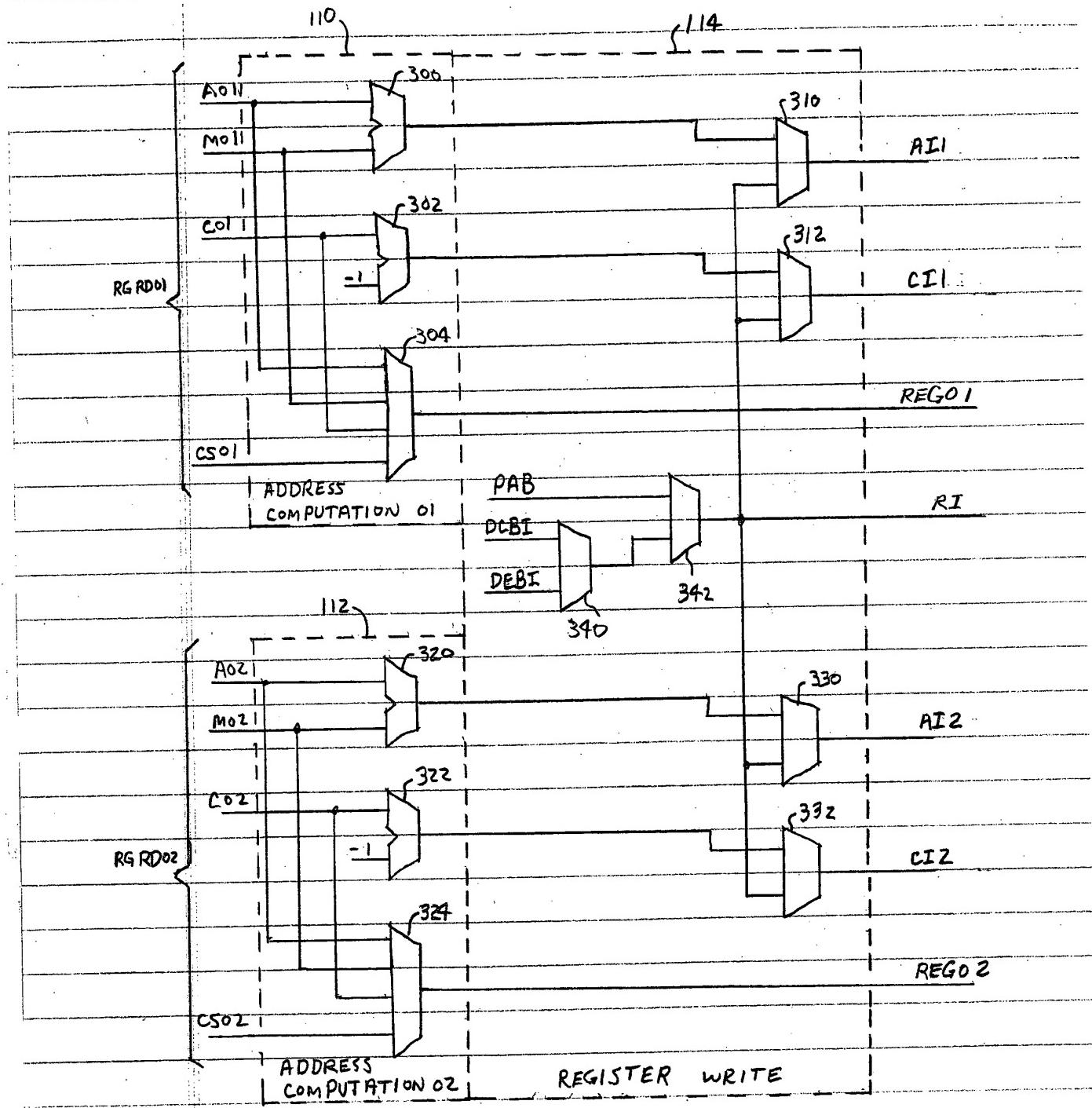


FIG. 5

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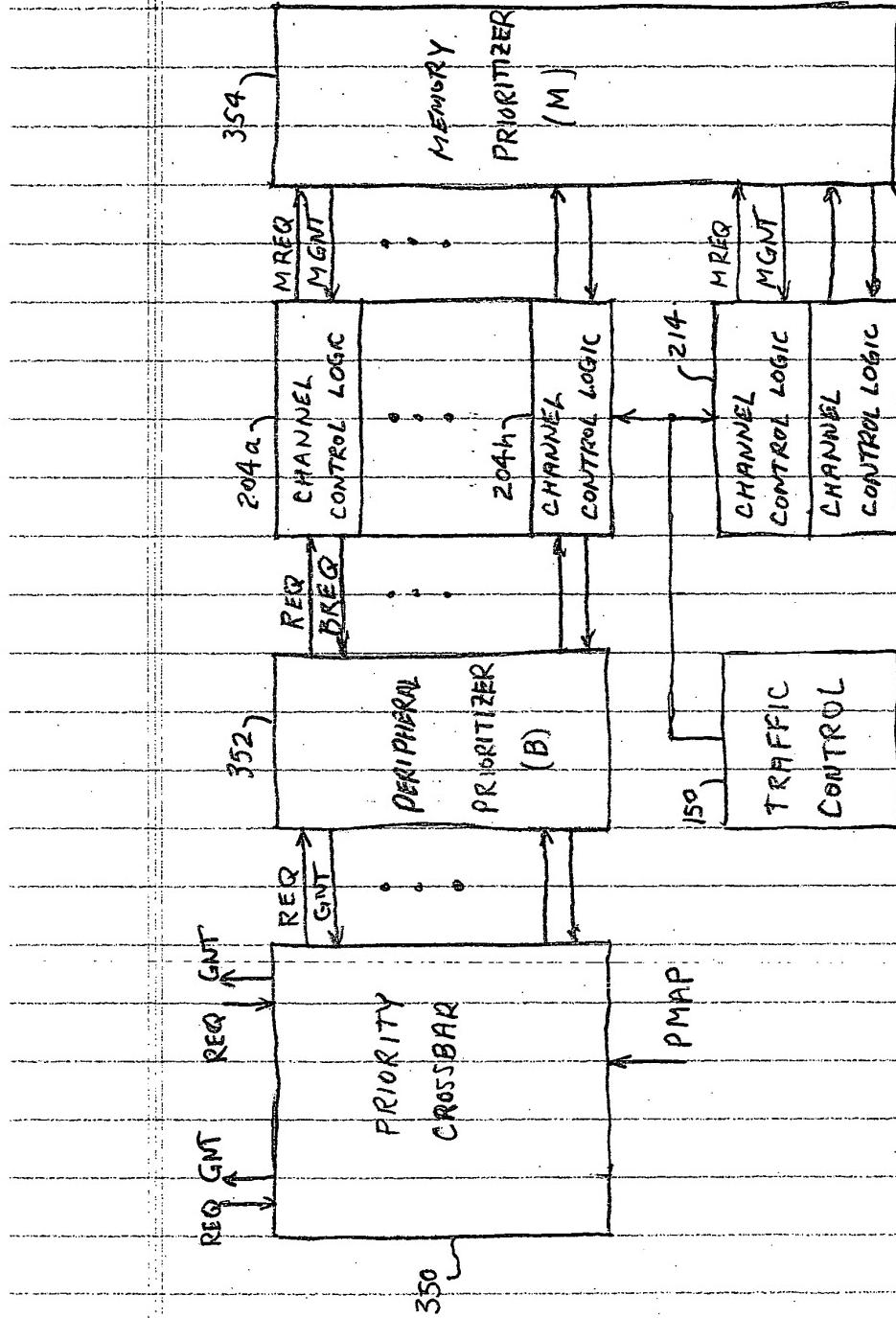


FIG. 6

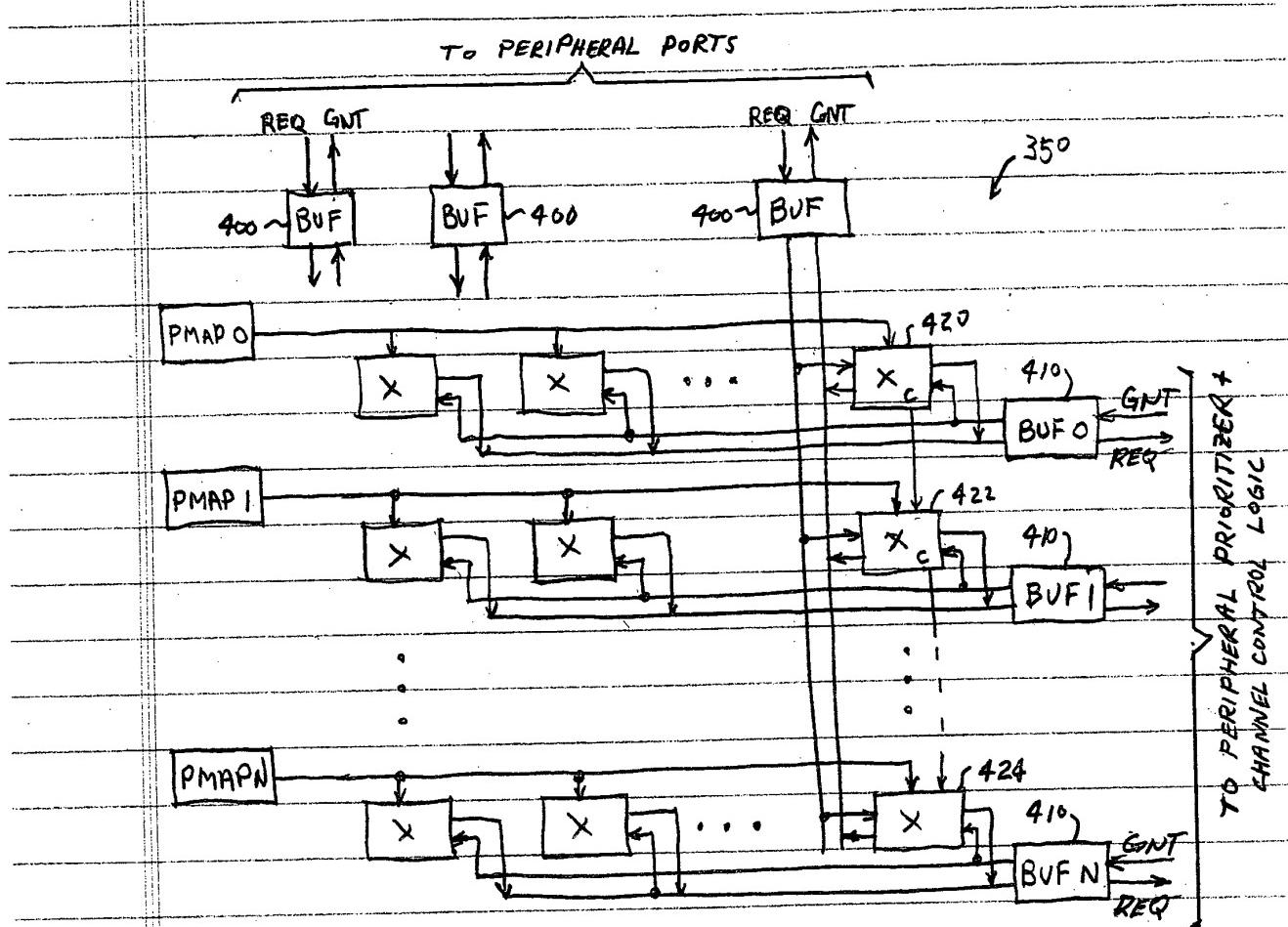


FIG. 7A

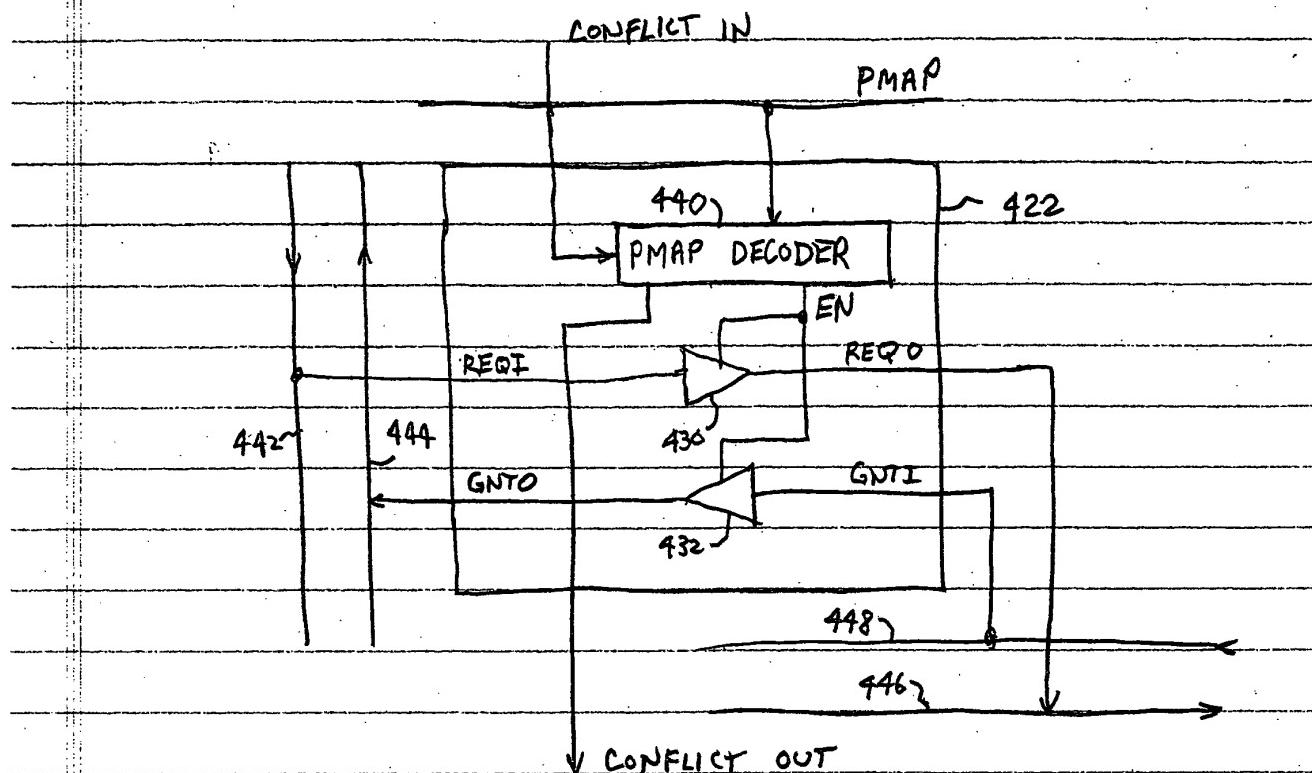


FIG. 7B

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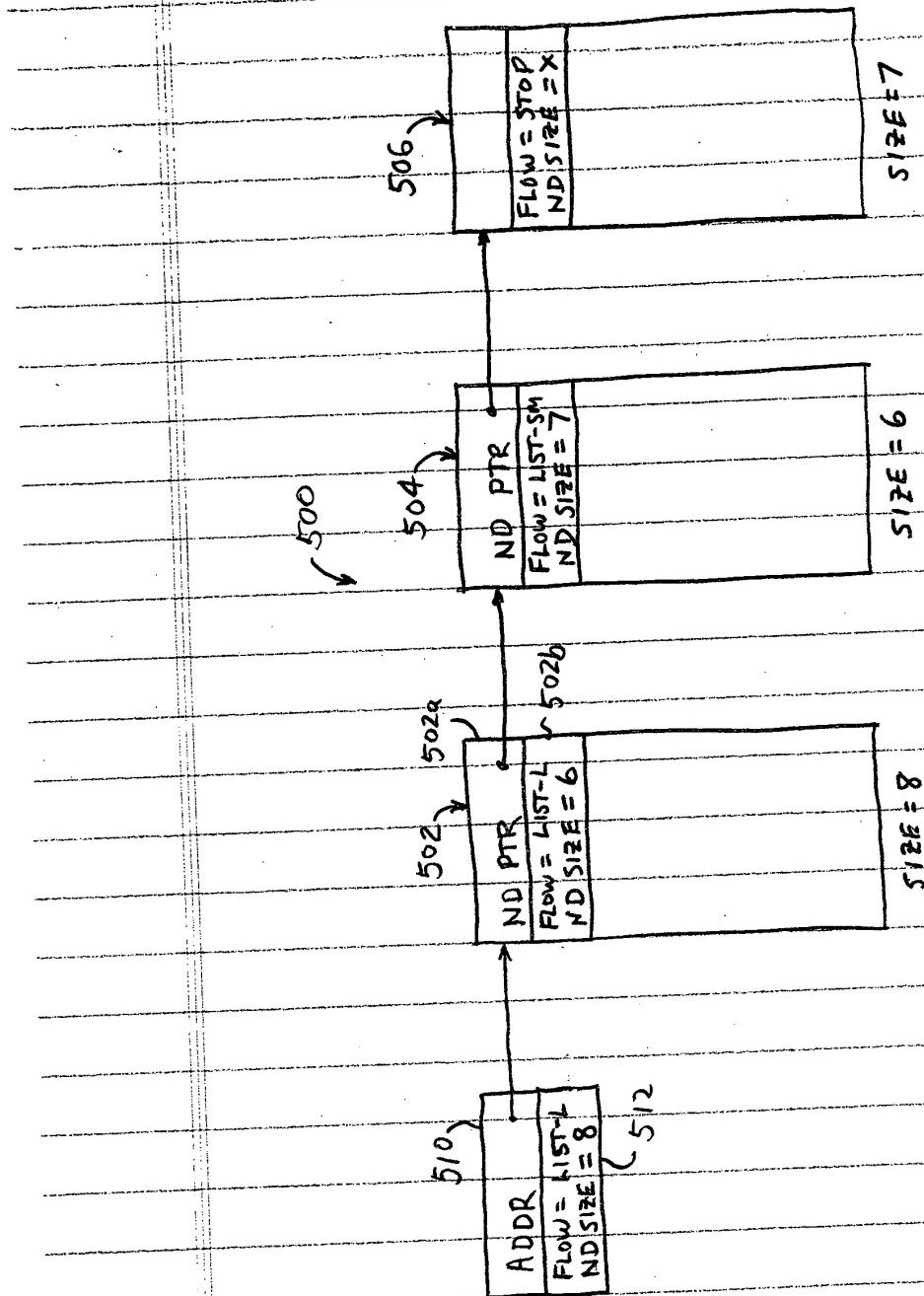


FIG. 8

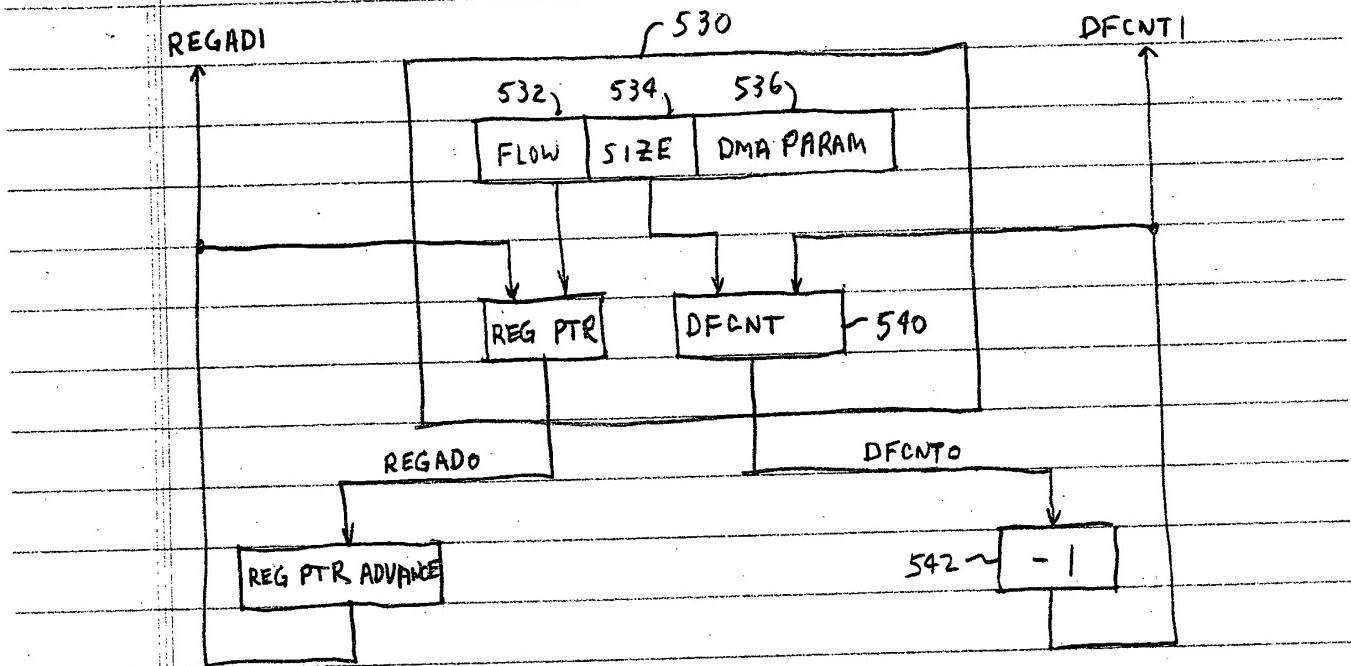


FIG. 9

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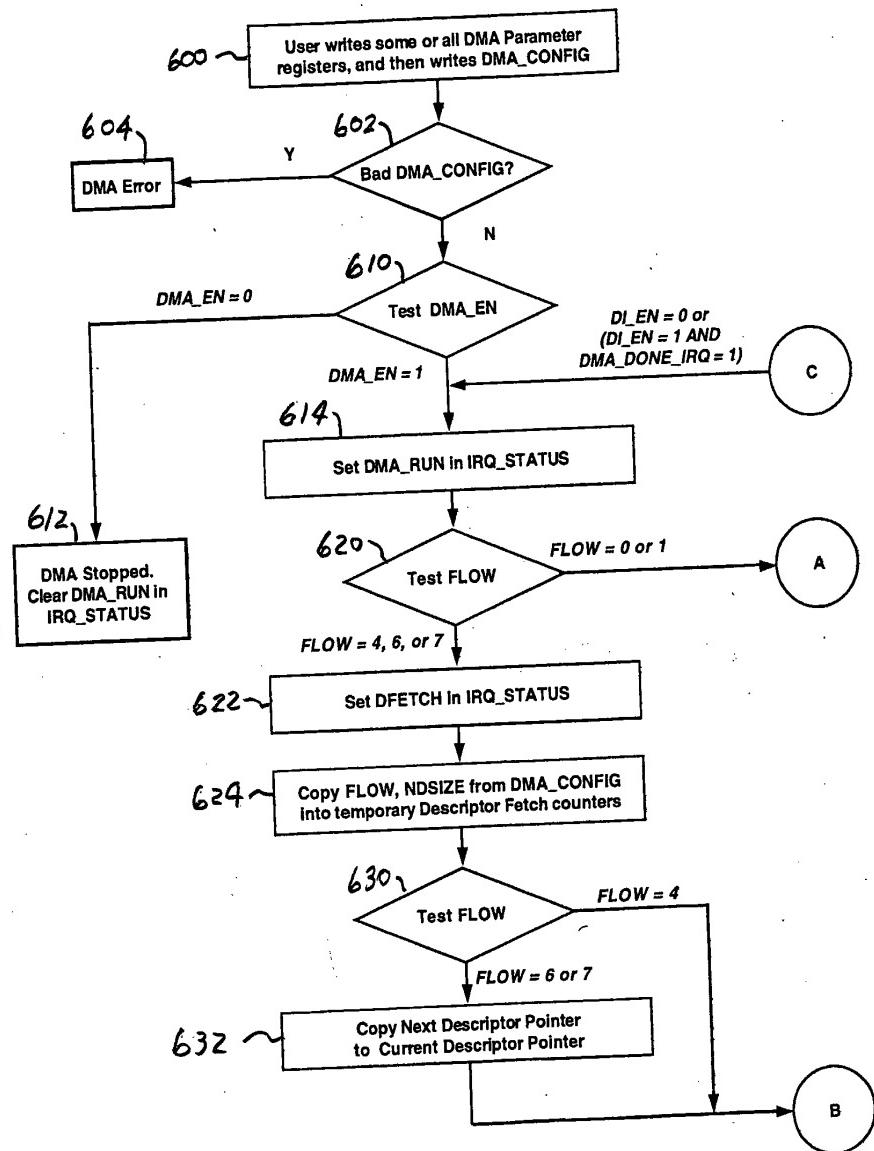


FIG. 10

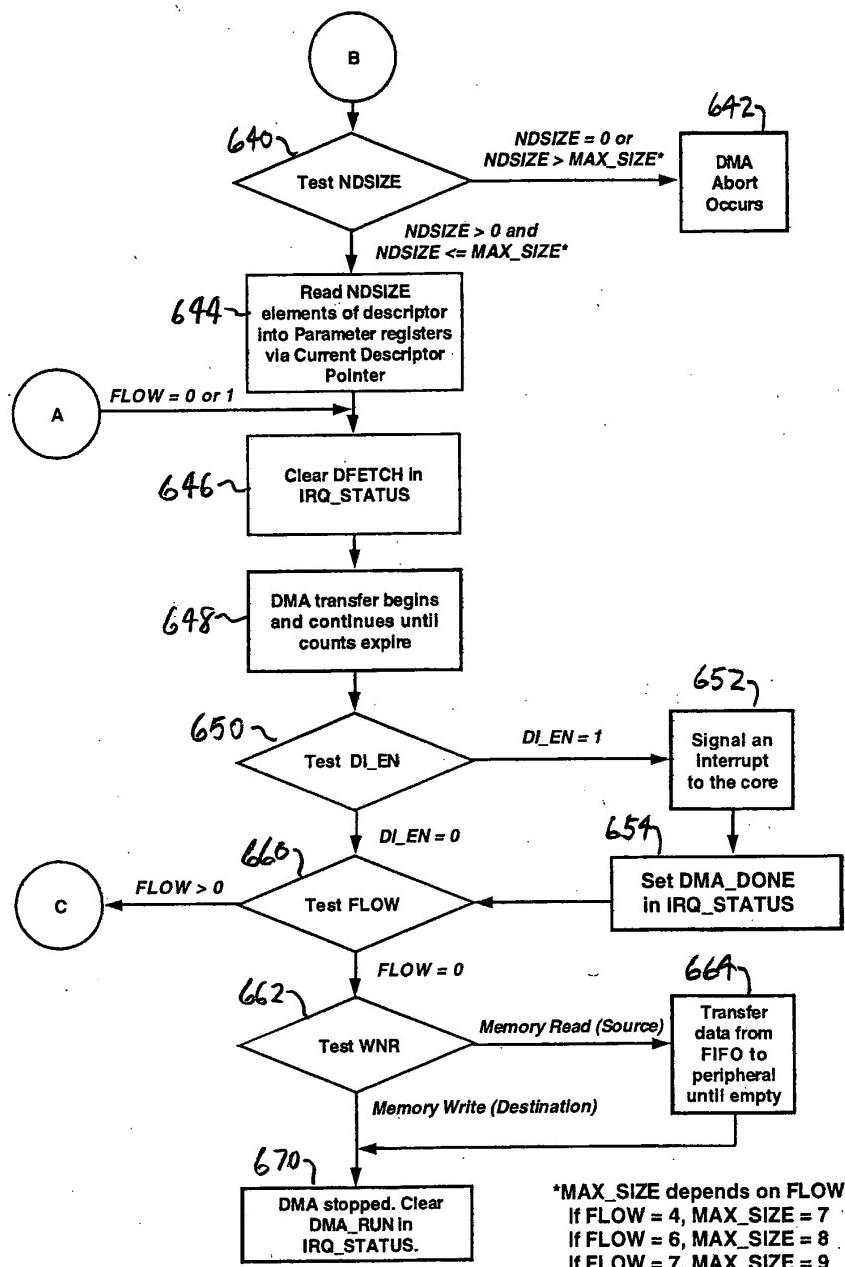


FIG. 11